

جامعة بنها - كلية العلوم - قسم الرياضيات
مادة من المستوي الثالث (علوم حاسب)

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المادة : معالجات دقيقة (325 رس)

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مدرس بقسم الرياضيات بكلية العلوم

الاسئلة و نموذج الإجابة

ورقة كاملة

Model Answer

Answer of Question 1

A- **registers** are some internal memory storage locations in processor to speed up the processor operations.

cache memory: is a type of memory used to hold frequently used data. Cache memory is relatively small, but very fast

data bus transfers instructions and data between the CPU and memory

B-

	Speed	Data Bus Width	Memory Size
Processor			
8086	10MHZ	16	1M
80286	12.5MHZ	16	16M
80386	40MHZ	32	4G
80486	100MHZ	32	4G + 8K cache

C-

Segment Registers

Segments are specific areas defined in a program for containing data, code and stack. There are three main segment registers

1- CS register (code segment register) stores the starting address of the code segment (contains all the instructions to be executed).

Instruction pointer register is used to determine the offset

2- DS register (data segment register) stores the starting address of the data segment

3- SS register (Stack segment register) stores the starting address of the stack segment (holds local variables and functions parameters).

ESP register is used to determine the offset

D-

EAX is automatically used by multiplication and division instructions. It is often called the *extended accumulator* register.

EBX (base register) used in indexed addressing

ECX (count register) store the loop count in iterative operations

EDX (data register) used in I/O operations

EBP is used by high-level languages to reference function parameters and local variables on the stack. It should not be used for ordinary arithmetic or data transfer except at an advanced level of programming. It is often called the *extended frame pointer* register.

Answer of Question 2

A- An instruction contains four basic parts:

- Label (optional)
- Instruction mnemonic (required)
- Operand(s) (usually required)
- Comment (optional)

This is the basic syntax:

[label:] mnemonic [operands] [;comment]

B- MOV is very flexible in its use of operands, as long as the following rules are observed:

- Both operands must be the same size.
 - Both operands cannot be memory operands.
 - CS, EIP, and IP cannot be destination operands.
 - An immediate value cannot be moved to a segment register.
- Here is a list of the general variants of MOV, excluding segment registers:**

MOV reg, reg

MOV mem, reg

MOV reg, mem

MOV mem, imm

MOV reg, imm

C-val1 BYTE ?

val2 SDWORD ?

D-

- a- EAX=00000000**
- b- EAX=00000027**
- c- EAX=00001100**
- d- EAX=35126579**
- e- EAX=35126500**
- f- EAX=35126550**
- g-EAX=35120100**
- h-EAX=00050000**
- i- EAX=00000004**
- j- EAX=00000005**

E-

- a- array of word type and its length is 22**
- b- array of word type and its length is 12**
- c- the next variable is aligned on an even-numbered address**
- d- EAX = 00001269**
- e- The OFFSET operator returns the offset of a data label. The offset represents the distance, in bytes, of the label from the beginning of the data segment.**

Example:

.data

```

buffer BYTE ?
buffer1 WORD ?
buffer2 DWORD ?
mov edx,OFFSET buffer ; EDX = 00404000
mov edx,OFFSET buffer1 ; EDX = 00404001
mov edx,OFFSET buffer2 ; EDX = 00404003

```

Answer of Question 3

A-

Nested Loops When creating a loop inside another loop, special consideration must be given to the outer loop counter in ECX. You can save it in a variable:

```

.data
count DWORD ?
.code
mov ecx,100 ; set outer loop count
L1:
mov count,ecx ; save outer loop count
mov ecx,20 ; set inner loop count
L2:
.
.
loop L2 ; repeat the inner loop
mov ecx,count ; restore outer loop count
loop L1 ; repeat the outer loop

```

As a general rule, nested loops more than two levels deep are difficult to write. If the algorithm you're using requires deep loop nesting, move some of the inner loops into subroutines.

B-

TITLE arithmetic expression : $Rval = -Xval + (Yval - Zval)$;

INCLUDE Irvine32.inc

```

.data
Rval SDWORD ?
Xval SDWORD 26
Yval SDWORD 30
Zval SDWORD 40
.code
main PROC
; first term: -Xval
mov eax,Xval
neg eax ; EAX = -26
; second term: (Yval - Zval)
mov ebx,Yval
sub ebx,Zval ; EBX = -10
; add the terms and store:
add eax,ebx

```

```
mov Rval,eax ; -36
exit
main ENDP
END main
```

C-

```
TITLE MsgBoxAsk
INCLUDE Irvine32.inc
.data
caption BYTE "Survey Completed",0
question BYTE "Thank you for completing the survey."
BYTE 0dh,0ah
BYTE "Would you like to receive the results?",0
.code
main PROC
mov ebx,OFFSET caption
mov edx,OFFSET question
call MsgBoxAsk
;(check return value in EAX)
exit
main ENDP
END main
```

D-

```
TITLE Copying a String (CopyStr.asm)
INCLUDE Irvine32.inc
.data
source BYTE "This is the source string",0
target BYTE SIZEOF source DUP(0)
.code
main PROC
mov esi,0 ; index register
mov ecx,SIZEOF source ; loop counter
L1:
mov al,source[esi] ; get a character from source
mov target[esi],al ; store it in the target
inc esi ; move to next character
loop L1 ; repeat for entire string
exit
main ENDP
END main
```

E-

```
ArraySum PROC
;
; Calculates the sum of an array of 32-bit integers.
; Receives: ESI = the array offset
; ECX = number of elements in the array
```

```
; Returns: EAX = sum of the array elements  
;-----  
push esi ; save ESI, ECX  
push ecx  
mov eax,0 ; set the sum to zero  
L1: add eax,[esi] ; add each integer to sum  
add esi,TYPE DWORD ; point to next integer  
loop L1 ; repeat for array size  
pop ecx ; restore ECX, ESI  
pop esi  
ret ; sum is in EAX  
ArraySum ENDP
```